

FIG. 2 - Prior Art

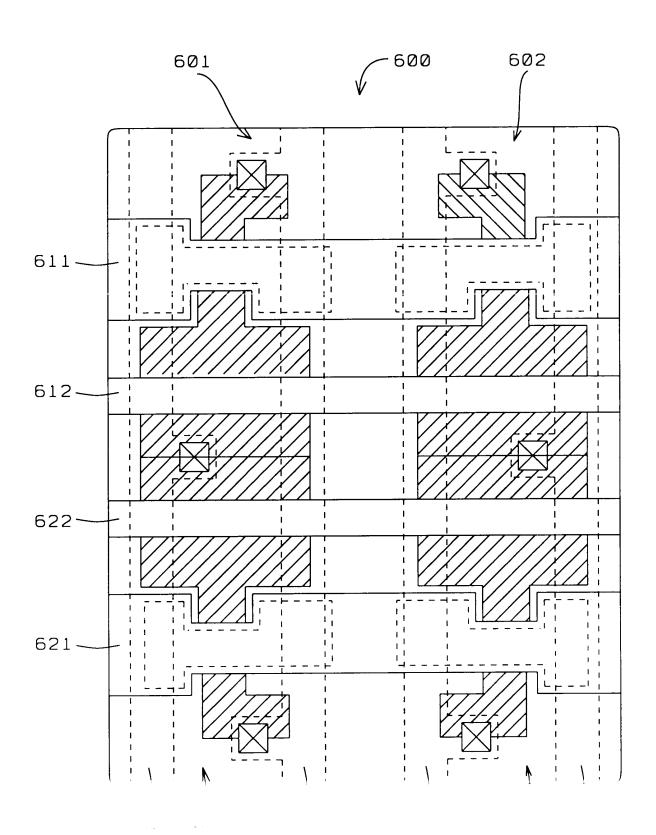


FIG. 3 - Prior Art

						+	0			0		4	16	
	V nwell	3.3 VOLTS	3.3 VOLTS	3.3 VOLTS	Ø VOLTS 3.3 VOLTS	r Art	300	BULK O		4 4 0		4		5
	V pwell		Ø VOLTS Ø VOLTS	8 TO 10 VOLTS	Ø VOLTS	Prior			7.19					FIG.
	٧d	5 TO 8 VO_TS	Ø VOLTS	Z НЭÏН	1 VOLT	- P	li I	DRAI Q	717	+ -				H
	٧S	2 нЭІн	д нэін	-8 TO -10 VOLTS	Ø VOLTS	4	SS GATE	0 18						
Non-Selected WL	Vag	Ø VOLTS	SITON Ø		SITON Ø	FIG.	ACCESS	13			1	1	0	
Non-Se	Vcg	Ø VOLTS	Ø VOLTS		Ø VOLTS	H	АТЕ	$\begin{array}{c} 10 \\ 11 \\ 1 \end{array}$	15 (+ C	pwel1	nwe]]	psub	ത
۸L	Vag	8 VOLTS	8 VOLTS	Ø VOLTS	3.3 VOLTS		CONTROL GATE			7				68
Selected	Vcg	-7 TO -11 VOLTS	-7 TO -11 VOLTS	8 TO 100 VOLTS	3.3 VOLTS		0	JURCE Q 14\B		+ C				38
	2				٠	1		UC			;			
	Σ	PROL	PROC INHI	ER7	Rf					L				

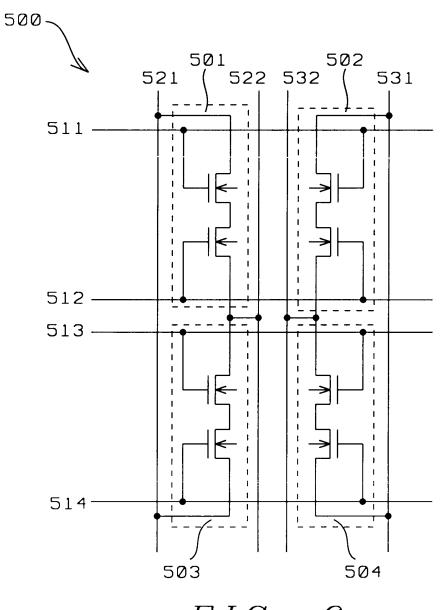


FIG. 6

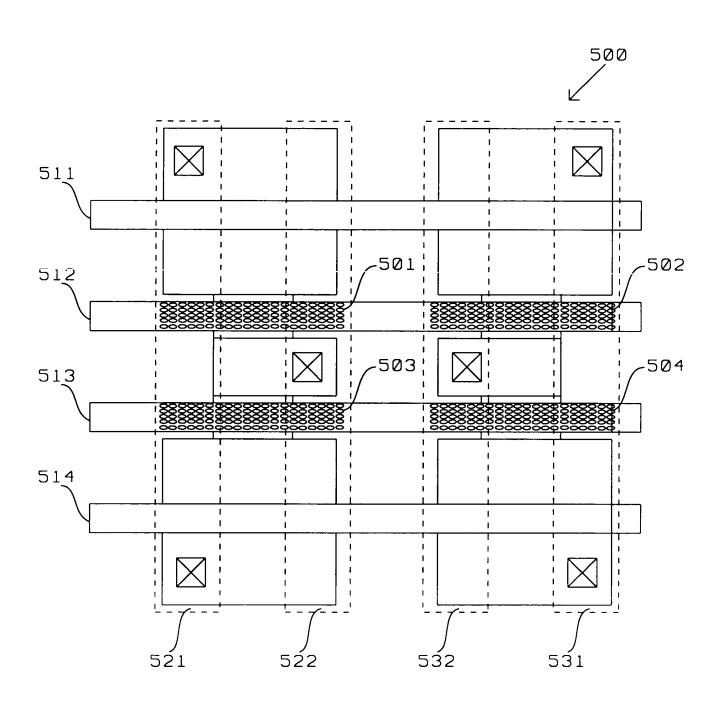


FIG. 7

	Selected WL	M	Non-Sele	Non-Selected WL			
JOE	Vcg	Vag	Vcg	Vag	٧5	۸م	V pwell
≀ASE	-10 VOLTS 0 VOLT	Ø VOLT	1	1	S VOLTS HIGH Z 5 VOLTS	2 нэін	5 VOLTS
GRAM	MbdV	Vcc	-2.5 VOLTS	VCC	нісн 2	-5 VOLTS	-5 VOLTS
GRAM IBIT	νbdΛ	Vcc	-2.5 VOLTS	VCC	д нэгн	HIGH Z Ø VOLTS	-5 VOLTS
ΑО	Vcc	Vcc	Ø VOLTS	Ø VOLTS	Ø VOLTS Ø VOLTS Ø VOLTS 1 VOLT Ø VOLTS	1 VOLT	Ø VOLTS

FIG. 8a

	Selected WL	ML	Non-Sel	Non-Selected WL			
)DE	Vcg	Vag	Vcg	Vag	٧۶	Vd V psub	V psub
ASE	-15 VOLTS 0 VOLT	Ø VOLT	I	I	Ø VOLTS HIGH Z Ø VOLTS	д нэтн	Ø VOLTS
GRAM	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	8 VOLTS HIGH Z @ VOLTS @ VOLTS	Ø VOLTS	Ø VOLTS
GRAMIBIT	Vpgm	8 VOLTS	2.5 VOLTS	8 VOLTS	2.5 8 VOLTS HIGH Z 5 VOLTS @ VOLTS	5 VOLTS	Ø VOLTS
ΔD	VCC	VCC	Ø VOLTS	Ø VOLTS	Ø VOLTS Ø VOLTS Ø VOLTS 1 VOLT Ø VOLTS	1 VOLT	Ø VOLTS

FIG. 8b